# MMFTP84 P-Channel Enhancement Mode Vertical D-MOS Transistor

#### **FEATURES**

- Low threshold voltage
- Direct interface to C-MOS, TTL, etc.

• Line current interrupter in telephone sets

• Relay, high speed and line transformer drivers

• High-speed switching

**APPLICATIONS** 

• No secondary breakdown



1. Gate 2. Source 3. Drain SOT-23 Plastic Package



### CAUTION

- The device is supplied in an antistatic package
- The gate-source input must be protected against static discharge during transport or handling

#### **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	-V <sub>DS</sub>	50	V	
Gate-Source Voltage	V <sub>GSO</sub>	± 20	V	
Drain Current	-I <sub>D</sub>	130	mA	
Peak Drain Current	-I <sub>DM</sub>	520	mA	
Total Power Dissipation at $T_{amb} \le 25 \ ^{\circ}C$	P <sub>tot</sub>	250 <sup>1)</sup>	mW	
Operating Junction Temperature	Tj	150	°C	
Storage Temperature	T <sub>stg</sub>	-65 to +150	°C	

#### **Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance from Juntion to Ambient	R <sub>thj-a</sub>	500 <sup>1)</sup>	K/W
<sup>1)</sup> Device mounted on a printed circuit board			

<sup>1</sup> Device mounted on a printed-circuit board.



# Characteristics at $T_j = 25 \ ^{\circ}C$ unless otherwise specified

Parameter	Symbol	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage		s 50	-	-	V
at -I <sub>D</sub> = 10 μA	-V <sub>(BR)DSS</sub>				
Gate-Source Threshold Voltage	-V <sub>GSth</sub>	0.0	3 -	2	V
at $V_{DS} = V_{GS}$ , $-I_D = 1 \text{ mA}$		0.8			
Drain-Source Leakage Current	-I <sub>DSS</sub>				
at -V <sub>DS</sub> = 40 V		-	-	100	nA
at -V <sub>DS</sub> = 50 V		-	-	10	μA
at -V <sub>DS</sub> = 50 V, T <sub>j</sub> = 125 ∘C		-	-	60	μA
Gate Leakage Current		-	-	± 10	nA
at $V_{GS}$ = ± 20 V	I <sub>GSS</sub>				
Drain-Source On-State Resistance	D		-	10	Ω
at -V <sub>GS</sub> = 10 V, -I <sub>D</sub> = 130 mA	R <sub>DSon</sub>				
Forward Transfer admittance		50	) _	-	mS
at -V <sub>DS</sub> = 25 V, -I <sub>D</sub> = 130 mA	y∫fs	50			
Input Capacitance	6		-	45	pF
at -V <sub>DS</sub> = 25 V, f = 1 MHz	Ciss	-			
Output Capacitance	0	C <sub>oss</sub> -	-	25	pF
at -V <sub>DS</sub> = 25 V, f = 1 MHz	C <sub>oss</sub>				
Reverse Transfer Capacitance	C <sub>rss</sub>	C <sub>rss</sub> -	-	12	pF
at -V <sub>DS</sub> = 25 V, f = 1 MHz					
Turn-On Time	t <sub>on</sub>	t <sub>on</sub> -	3	-	ns
at $V_{GS}$ = 0 to -10 V, -V <sub>DD</sub> = 40 V, -I <sub>D</sub> = 200 mA					
Turn-Off Time	t <sub>off</sub>		- 7	-	ns
at $V_{GS}$ = -10 to 0 V, -V <sub>DD</sub> = 40 V, -I <sub>D</sub> = 200 mA		-			



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## **PACKAGE OUTLINE**

#### Plastic surface mounted package; 3 leads

SOT-23

