

MAC97A6, MAC97A8

Sensitive Gate Triacs Silicon Bidirectional Thyristors

DESCRIPTION

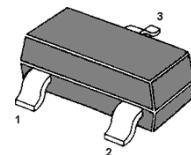
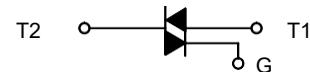
Logic level sensitive gate triac intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

FEATURES

- Blocking voltage to 400 V (MAC97A6)
- RMS on-state current to 0.6 A
- General purpose bidirectional switching

APPLICATIONS

- General purpose bidirectional switching
- Phase control applications
- Solid state relays



1. SOT-23 Plastic Package

2. MARKING:
MAC97A6:97A6
MAC97A8:97A8

PIN ASSIGNMENT	
1	Main Terminal 1
2	Gate
3	Main Terminal 2

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Value	Unit
V_{DRM} / V_{RRM}	repetitive peak off-state voltage MAC97A6 MAC97A8	$T_j = 25 \text{ to } 125^\circ\text{C}$ $T_j = 25 \text{ to } 125^\circ\text{C}$	400 600	V
I_{GM}	gate current(peak value)	$t = 2\mu\text{s}$ max	1	A
V_{GM}	gate voltage(peak value)	$t = 2\mu\text{s}$ max	5	V
P_{GM}	gate power(peak value)	$t = 2\mu\text{s}$ max	5	W
T_j	Junction Temperature	-	-40 ~ 125	$^\circ\text{C}$
T_{stg}	Storage Temperature	-	-40 ~ 150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Max	Unit
Rated repetitive peak off-state/reverse voltage	V_{DRM}, V_{RRM}	$I_D=10\mu\text{A}$ MAC97A6 MAC97A8	400 600		V
Rated repetitive peak off-state current	I_{DRM}	$V_D=V_{DRM}$		10	μA
On-state voltage	V_{TM}	$I_T=1\text{A}, I_G=50\text{mA}$		1.9	V
Gate trigger current	I	I_{GT}	$T_2(+), G(+)$	$V_D=12\text{V}$ $R_L=100\Omega$	5 mA
	II		$T_2(+), G(-)$		5 mA
	III		$T_2(-), G(-)$		5 mA
	IV		$T_2(-), G(+)$		- mA
Gate trigger voltage	I	V_{GT}	$T_2(+), G(+)$	$V_D=12\text{V}$ $R_L=100\Omega$	1.5 V
	II		$T_2(+), G(-)$		1.5 V
	III		$T_2(-), G(-)$		1.5 V
	IV		$T_2(-), G(+)$		- V
Holding current	I_H	$I_T=600\text{mA}, I_G=20\text{mA}$		10	mA

Typical Characteristics

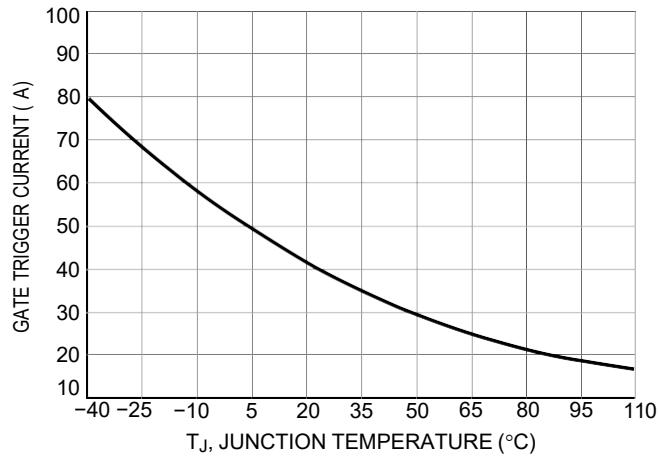


Figure 1. Typical Gate Trigger Current versus Junction Temperature

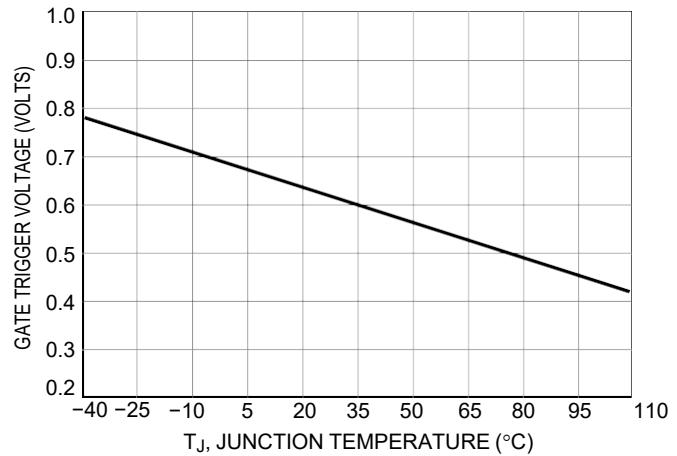


Figure 2. Typical Gate Trigger Voltage versus Junction Temperature

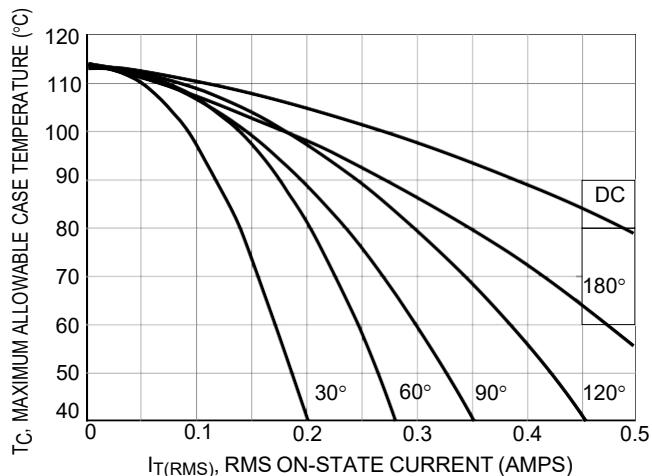


Figure 3 Typical RMS Current Derating

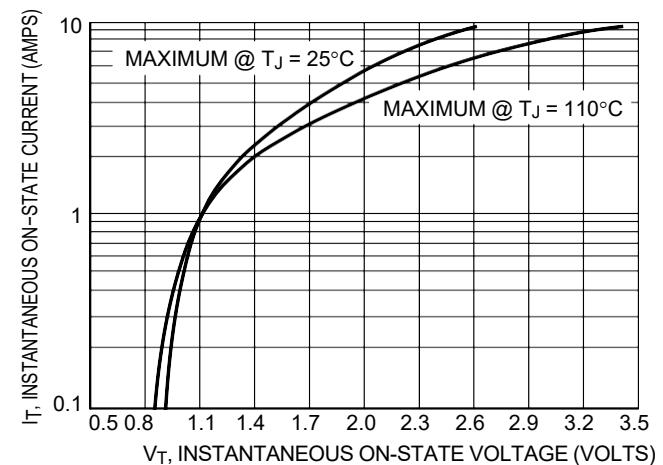
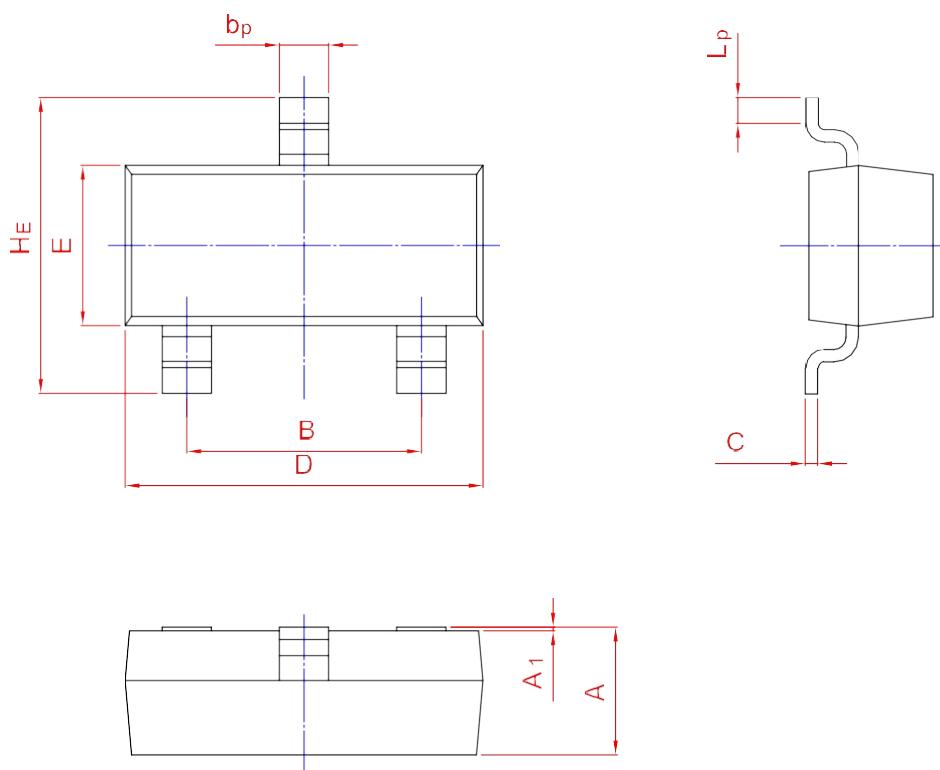


Figure 4. Typical On-State Characteristics

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



UNIT	A	B	b_p	C	D	E	H_E	A_1	L_p
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20