

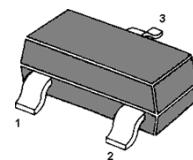
# MMBT6520 PNP High Voltage Transistor

## ■ Features

- PNP Silicon
- High Voltage Transistor

Marking: 2Z

SOT-23



1. BASE
2. Emitter
3. COLLECTOR

## Absolute Maximum Ratings Ta = 25

Parameter	Symbol	Rating	Unit
Collector-emitter voltage	VCEO	-350	V
Collector-base voltage	VCBO	-350	V
Emitter-base voltage	VEBO	-5	V
Base current	IB	-250	mA
Collector current-continuous	IC	-500	mA
Total device dissipation FR-5 board *1  @TA = 25°C derate above 25°C	PD	225 1.8	mW mW/°C
Thermal resistance, junction-to-ambient	R <sub>θJA</sub>	556	°C/W
Total device dissipation alumina substrate*2  @TA = 25°C derate above 25°C	PD	300 2.4	mW mW/°C
Thermal resistance, junction-to-ambient	R <sub>θJA</sub>	417	°C/W
Junction and storage temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

\* 1. FR-5 = 1.0 X 0.75 X 0.062 in.

\* 2. Alumina = 0.4 X 0.3 X 0.024 in. 99.5% alumina.

Electrical Characteristics  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Collector-emitter breakdown voltage	$V_{(\text{BR})\text{CE}0}$	$I_c = -1 \text{ mA}, I_b = 0$	-350			V
Collector-base breakdown voltage	$V_{(\text{BR})\text{CB}0}$	$I_c = -100 \mu\text{A}, I_e = 0$	-350			
Emitter-base breakdown voltage	$V_{(\text{BR})\text{EB}0}$	$I_e = -10 \mu\text{A}, I_c = 0$	-5			
Collector cutoff current	$I_{\text{CEO}}$	$V_{\text{CB}} = -250 \text{ V}, I_b = 0$			-50	nA
Emitter cutoff current	$I_{\text{EBO}}$	$V_{\text{EB}} = -4 \text{ V}, I_c$			-50	nA
DC current gain	$\text{h}_{\text{FE}}$	$I_c = -1.0 \text{ mA}, V_{\text{CE}} = -10 \text{ V}$	20			
		$I_c = -10 \text{ mA}, V_{\text{CE}} = -10 \text{ V}$	30			
		$I_c = -30 \text{ mA}, V_{\text{CE}} = -10 \text{ V}$	30		200	
		$I_c = -50 \text{ mA}, V_{\text{CE}} = -10 \text{ V}$	20		200	
		$I_c = -100 \text{ mA}, V_{\text{CE}} = -10 \text{ V}$	15			
Collector-emitter saturation voltage	$V_{\text{CE}(\text{sat})}$	$I_c = -10 \text{ mA}, I_b = -1 \text{ mA}$			-0.3	V
		$I_c = -20 \text{ mA}, I_b = -2 \text{ mA}$			-0.35	V
		$I_c = -30 \text{ mA}, I_b = -3 \text{ mA}$			-0.5	V
		$I_c = -50 \text{ mA}, I_b = -5 \text{ mA}$			-1	V
Base-emitter saturation voltage	$V_{\text{BE}(\text{sat})}$	$I_c = -10 \text{ mA}, I_b = -1 \text{ mA}$			-0.75	V
		$I_c = -20 \text{ mA}, I_b = -2 \text{ mA}$			-0.85	V
		$I_c = -30 \text{ mA}, I_b = -3 \text{ mA}$			-0.9	V
Base-emitter on voltage	$V_{\text{BE}(\text{on})}$	$I_c = -100 \text{ mA}, V_{\text{CE}} = -10 \text{ V}$			-2	V
Transition frequency	$f_T$	$I_c = -10 \text{ mA}, V_{\text{CE}} = -20 \text{ V}, f = 20 \text{ MHz}$	40		200	MHz
Collector-base capacitance	$C_{\text{cb}}$	$V_{\text{CB}} = -20 \text{ V}, f = 1 \text{ MHz}$			6	pF
Emitter-base capacitance	$C_{\text{eb}}$	$V_{\text{EB}} = -0.5 \text{ V}, f = 1 \text{ MHz}$			100	pF

## Typical Characteristics

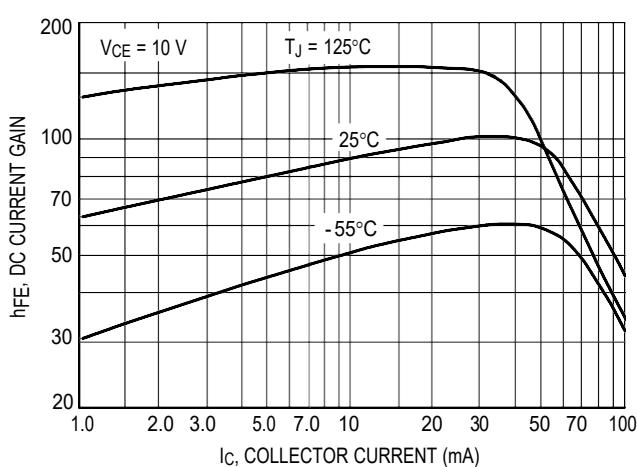


Figure 1. DC Current Gain

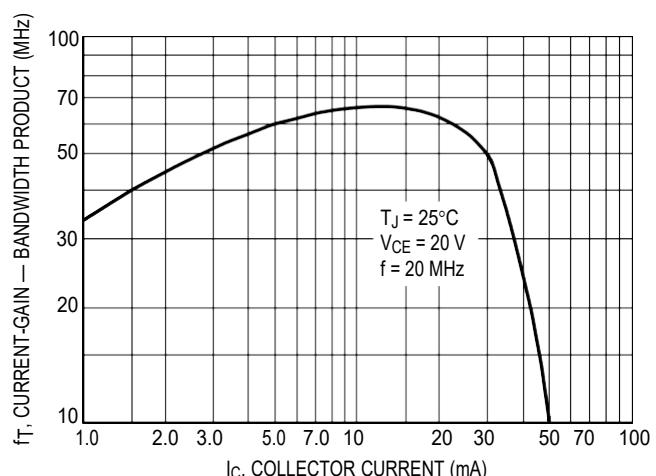


Figure 2. Current-Gain — Bandwidth Product

## Typical Characteristics

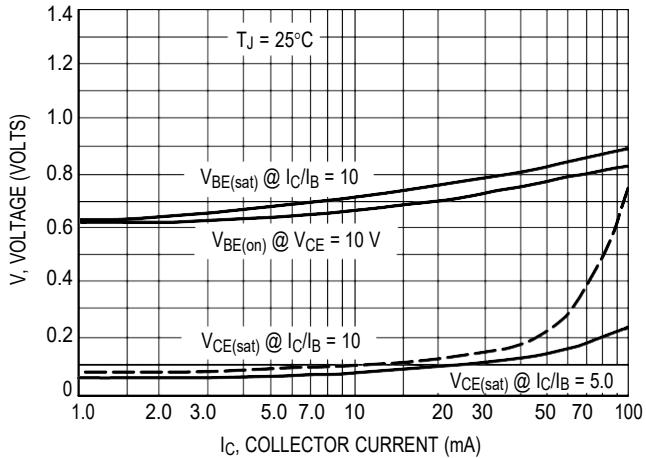


Figure 3. "On" Voltages

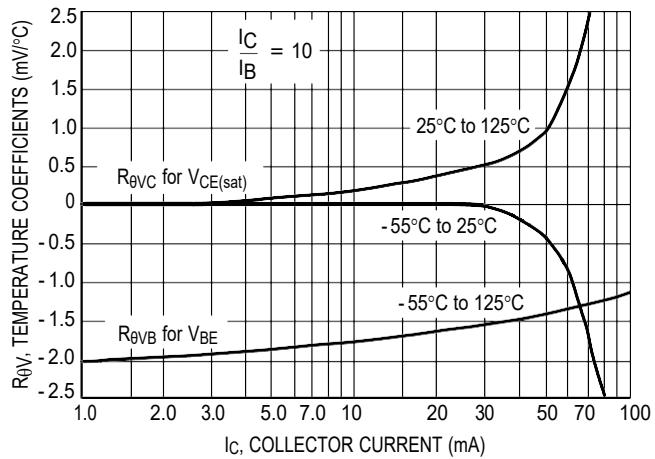


Figure 4. Temperature Coefficients

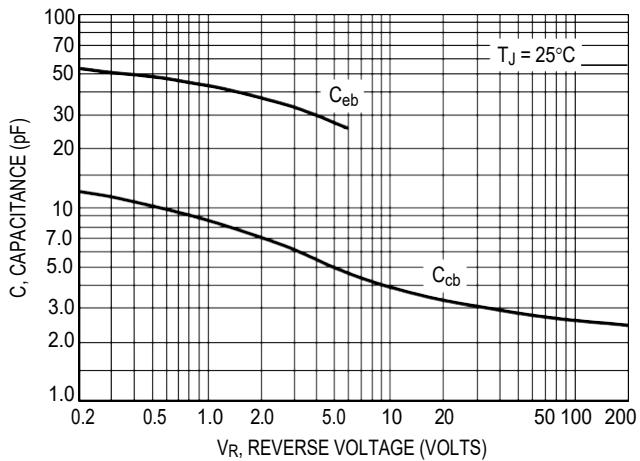


Figure 5. Capacitance

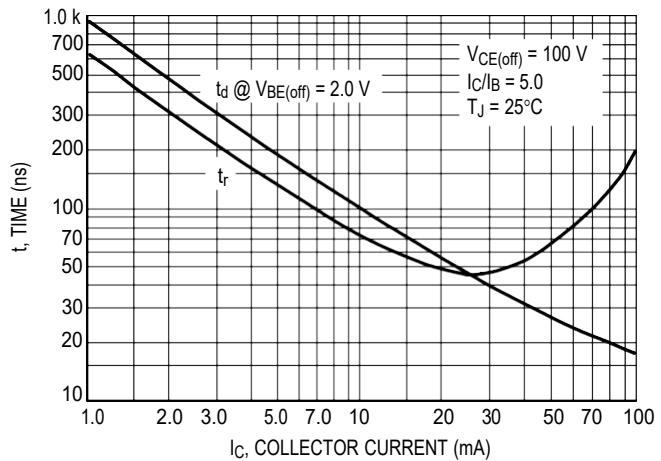


Figure 6. Turn-On Time

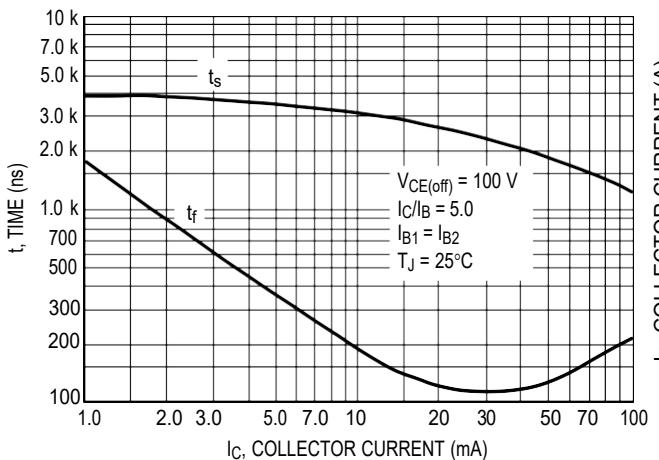


Figure 7. Turn-Off Time

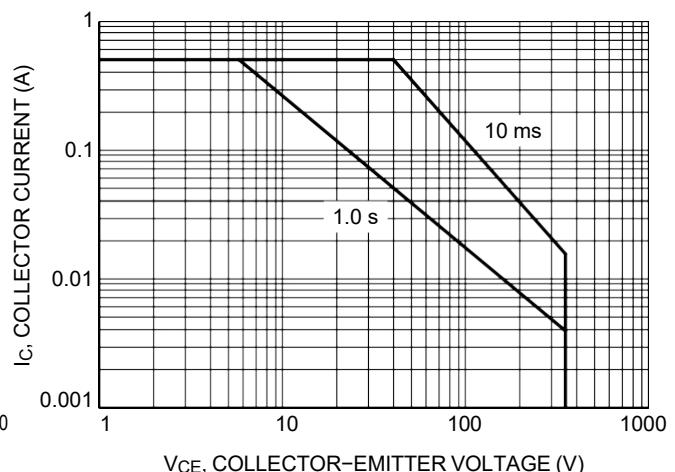


Figure 8. Safe Operating Area

## Typical Characteristics

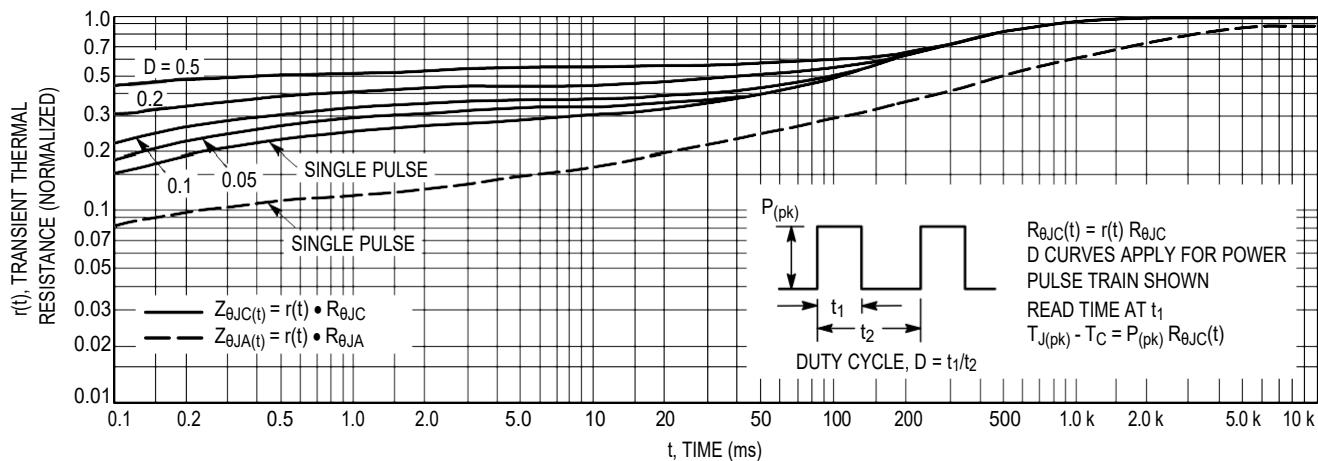


Figure 9. Thermal Response

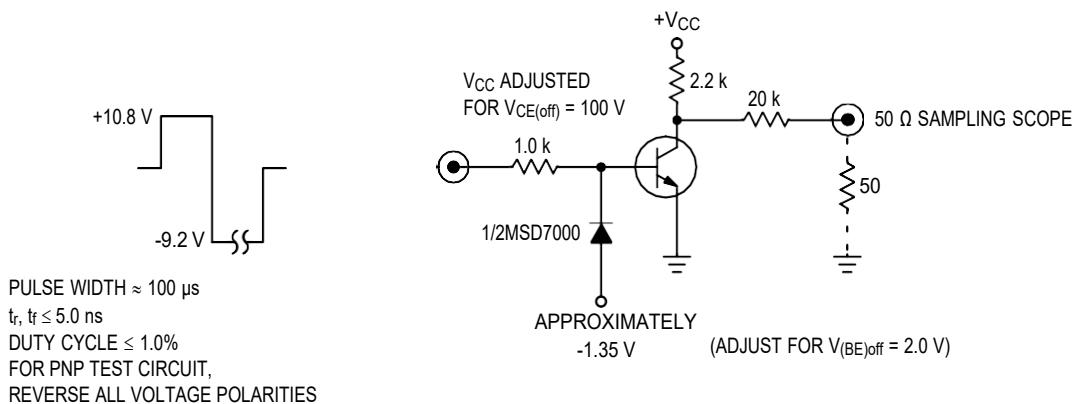
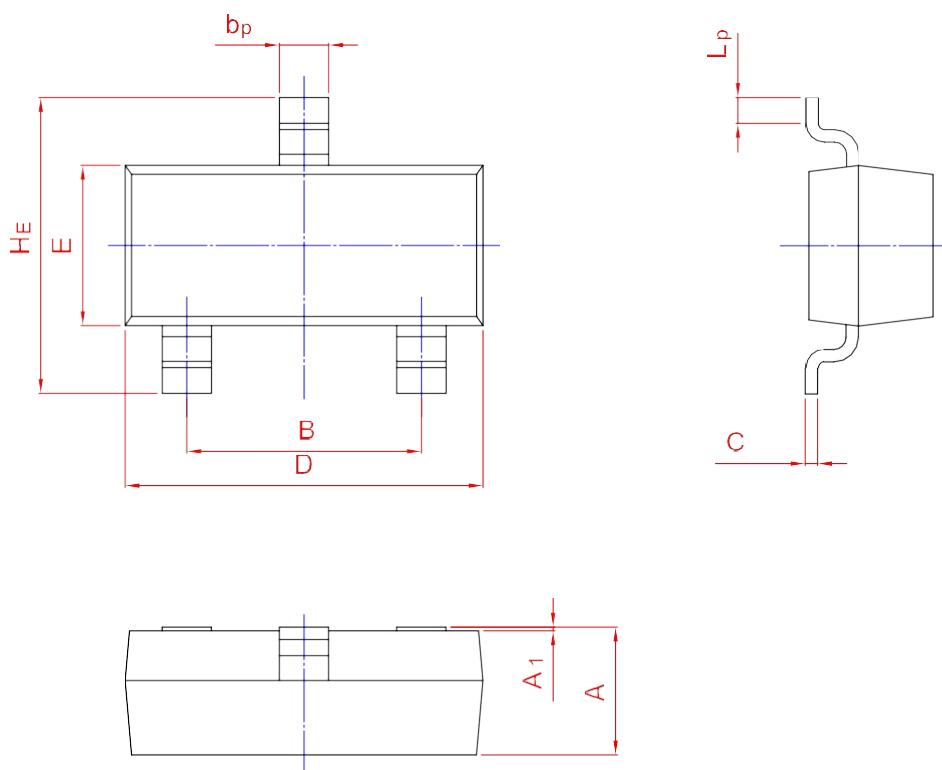


Figure 10. Switching Time Test Circuit

## PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



UNIT	A	B	$b_p$	C	D	$E$	$H_E$	$A_1$	$L_p$
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20