

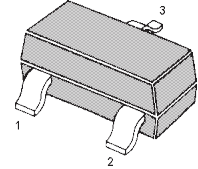
SOT-23 Plastic-Encapsulate Digital Transistors

DTC113ZCA DIGITAL TRANSISTOR (NPN)

FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

SOT-23



1. BASE
2. EMITTER
3. COLLECTOR

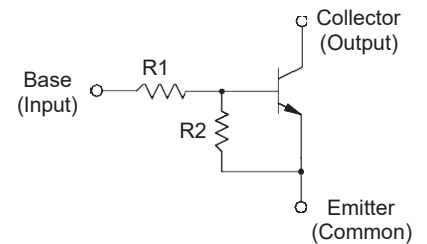
MARKING

Marking code:E21

MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	Limits	Unit
V_{CC}	Supply Voltage	50	V
V_{IN}	Input Voltage	-5~+10	V
I_O	Output Current	100	mA
P_D	Power Dissipation	200	mW
T_j	Junction Temperature	150	°C
T_{stg}	Storage Temperature	-55~+150	°C

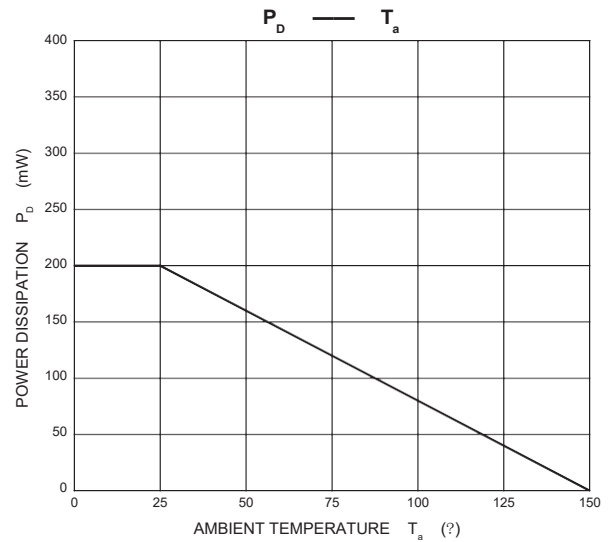
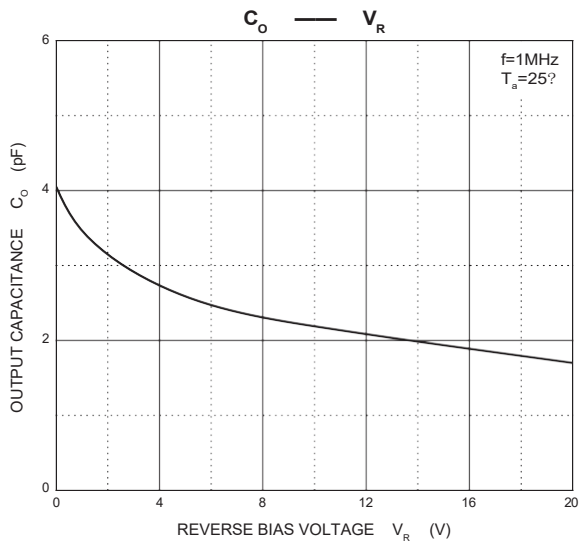
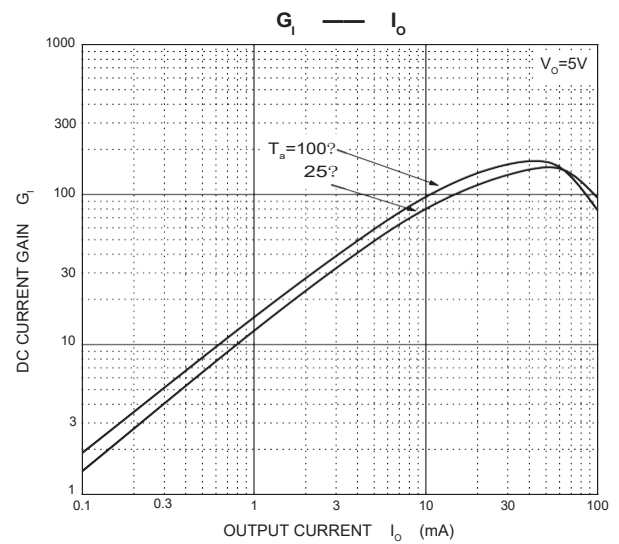
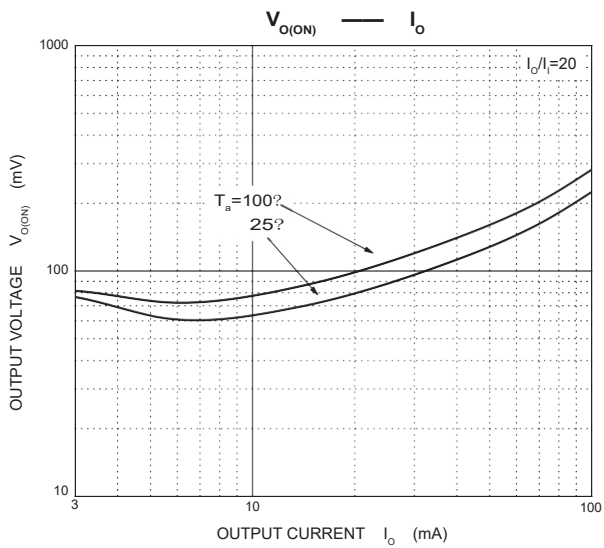
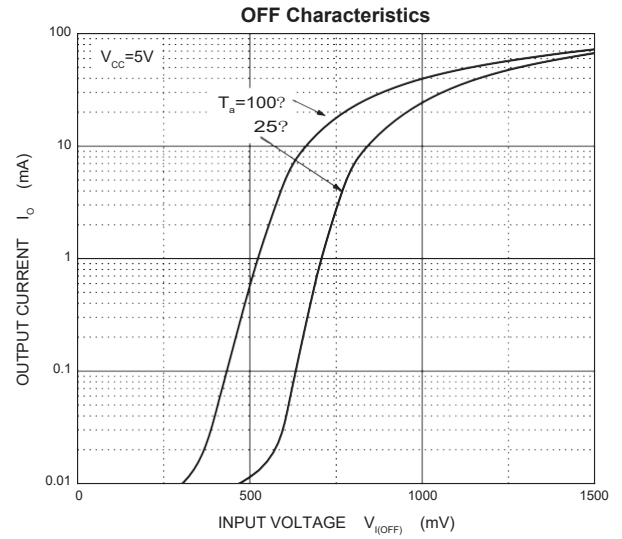
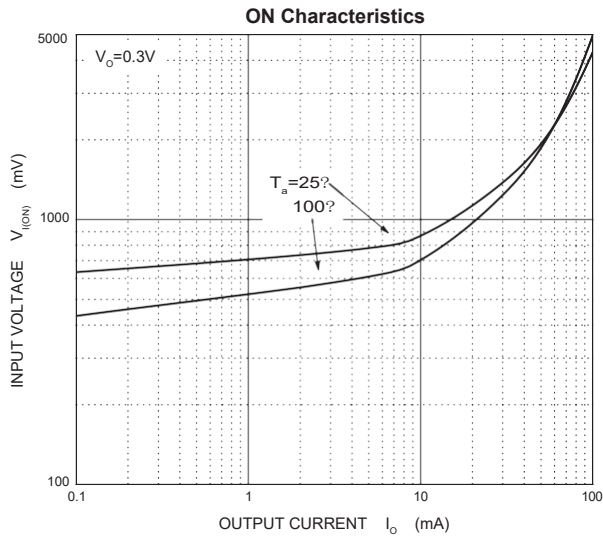
Equivalent Circuit



ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	$V_{I(off)}$	$V_{CC}=5V, I_O=100\mu A$	0.3			V
	$V_{I(on)}$	$V_O=0.3V, I_O=20mA$			3	V
Output voltage	$V_{O(on)}$	$I_O/I_I=10mA/0.5mA$			0.3	V
Input current	I_I	$V_I=5V$			7.2	mA
Output current	$I_{O(off)}$	$V_{CC}=50V, V_I=0$			0.5	μA
DC current gain	G_I	$V_O=5V, I_O=5mA$	33			
Input resistance	R_I		0.7	1	1.3	k Ω
Resistance ratio	R_2/R_1		8	10	12	
Transition frequency	f_T	$V_O=10V, I_O=5mA, f=100MHz$		250		MHz

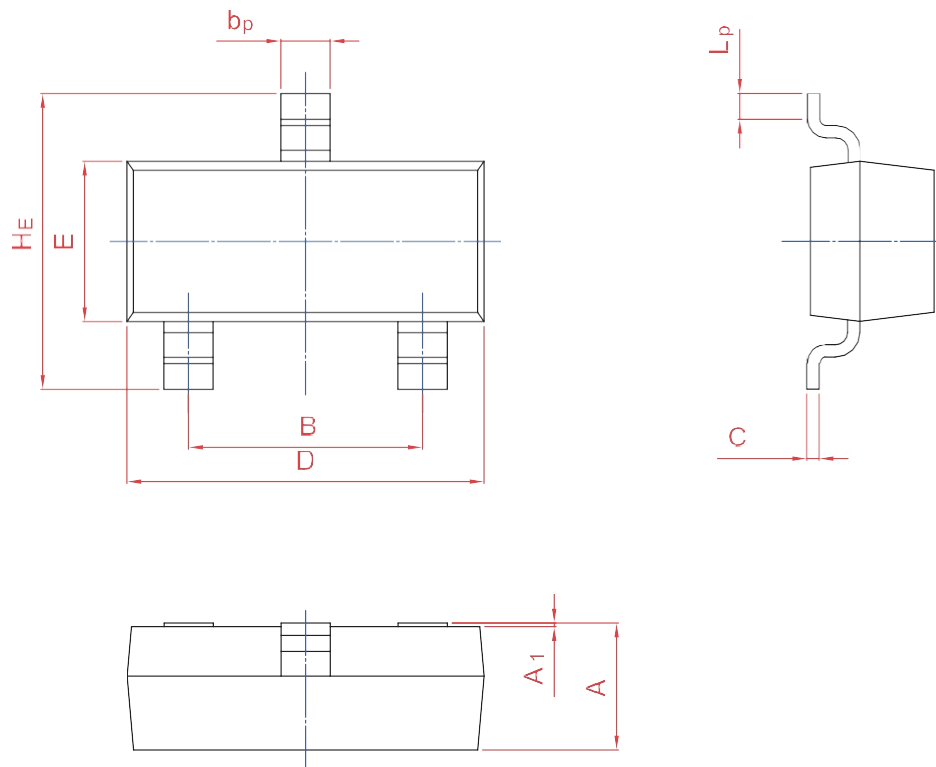
Typical Characteristics



PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



UNIT	A	B	bp	C	D	E	HE	A1	Lp
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20