

# DTA143XCA

DIGITAL TRANSISTOR (PNP)

## FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

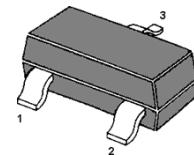
**MARKING:**X33

## Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

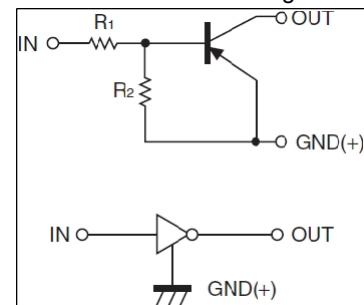
Symbol	Parameter	Min	Typ	Max	Unit
$V_{CC}$	Supply voltage	---	-50	---	V
$V_{IN}$	Input voltage	-20	---	7	V
$I_o$	Output current	---	-100	---	
$I_{C(MAX)}$	Output current	---	-100	---	
$P_d$	Power dissipation	---	200	---	mW
$T_j$	Junction temperature	---	150	---	?
$T_{stg}$	Storage temperature	-55	---	150	?

## ELECTRICAL CHARACTERISTICS ( $T_a=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ	Max.	Unit	Conditions
<b>Input voltage</b>	$V_{I(off)}$	-0.3			V	$V_{CC}=-5\text{V}, I_o=-100\mu\text{A}$
	$V_{I(on)}$			-2.5		$V_O=-0.3\text{V}, I_o=-20\text{ mA}$
<b>Output voltage</b>	$V_{O(on)}$		-0.1	-0.3	V	$I_o/I_i=-10\text{mA}/-0.5\text{mA}$
<b>Input current</b>	$I_I$			-1.8	mA	$V_I=-5\text{V}$
<b>Output current</b>	$I_{O(off)}$			-0.5	$\mu\text{A}$	$V_{CC}=-50\text{V}, V_I=0$
<b>DC current gain</b>	$G_I$	30				$V_O=-5\text{V}, I_o=-10\text{mA}$
<b>Input resistance</b>	$R_1$	3.29	4.7	6.11	$\text{K}\Omega$	
<b>Resistance ratio</b>	$R_2/R_1$	1.7	2.1	2.6		
<b>Transition frequency</b>	$f_T$		250		MHz	$V_O=-10\text{V}, I_o=5\text{mA}, f=100\text{MHz}$



1.Base(IN) 2.Emitter (GND)  
3.Collector(OUT)  
SOT-23 Plastic Package



**Equivalent Circuit**

## Typical Characteristics

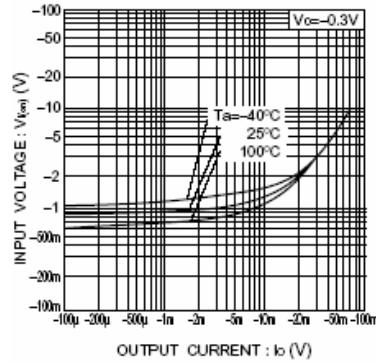


Fig.1 Input voltage vs. output current  
(ON characteristics)

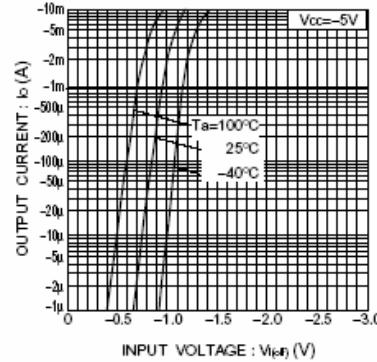


Fig.2 Output current vs. input voltage  
(OFF characteristics)

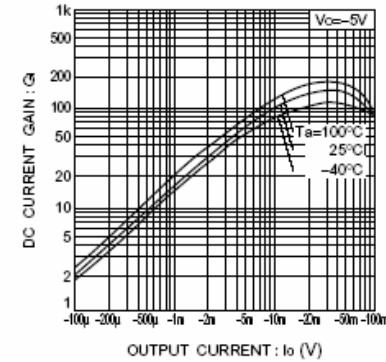


Fig.3 DC current gain vs. output current

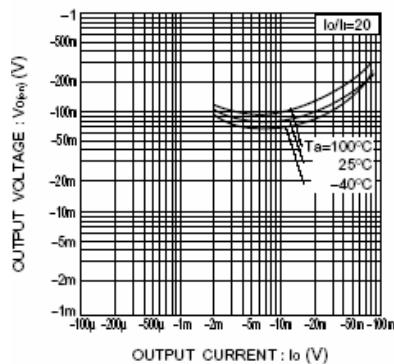
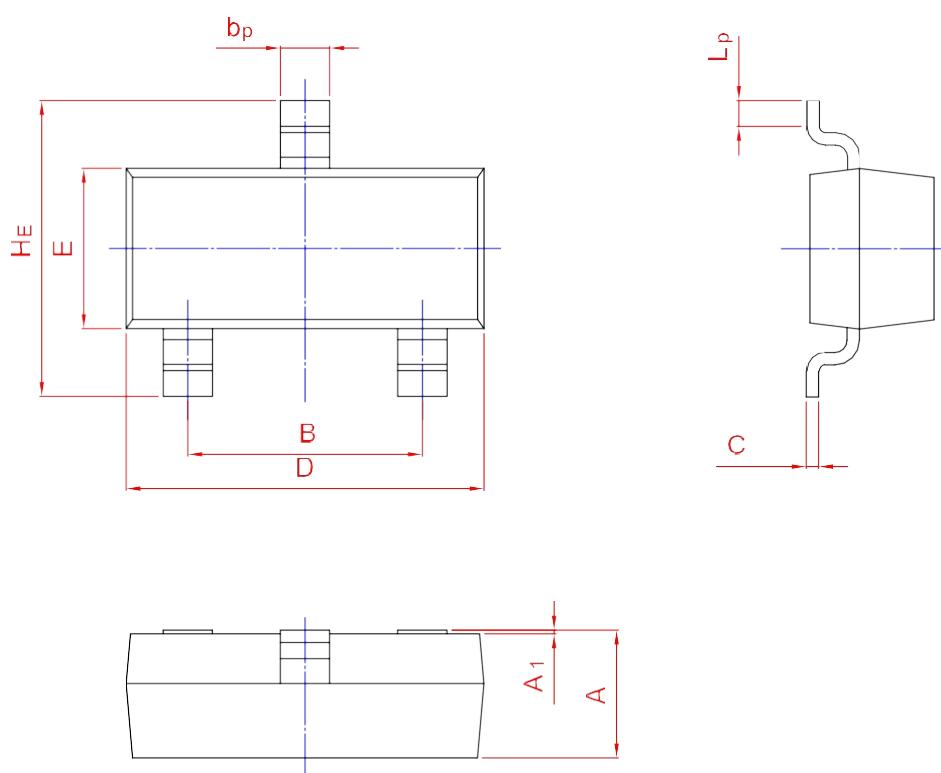


Fig.4 Output voltage vs. output current

## PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



UNIT	A	B	$b_p$	C	D	$E$	$H_E$	$A_1$	$L_p$
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20