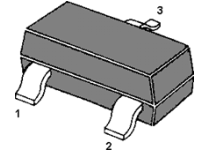
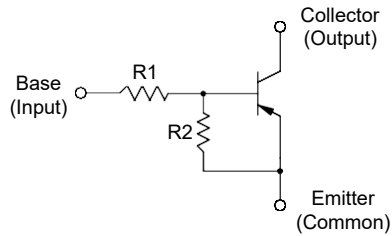


DTA114ECA PNP Silicon Epitaxial Planar Digital Transistor

FEATURES

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

MARKING: 14



1.Base 2.Emitter 3.Collector
SOT-23 Plastic Package

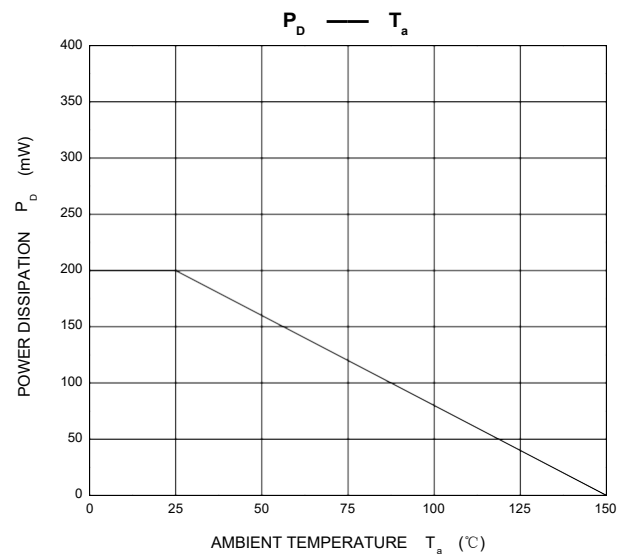
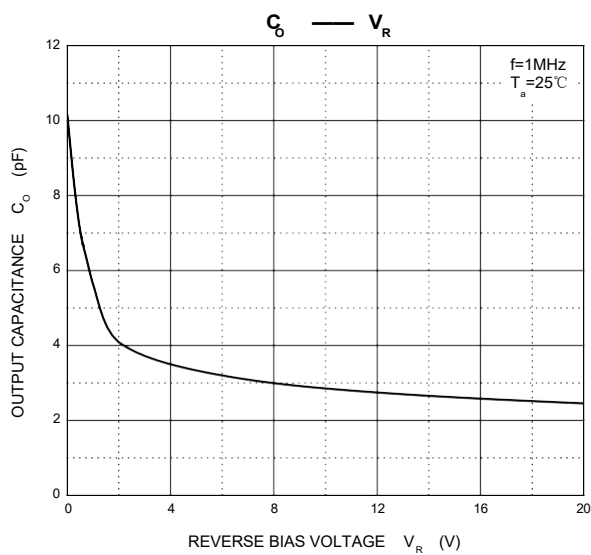
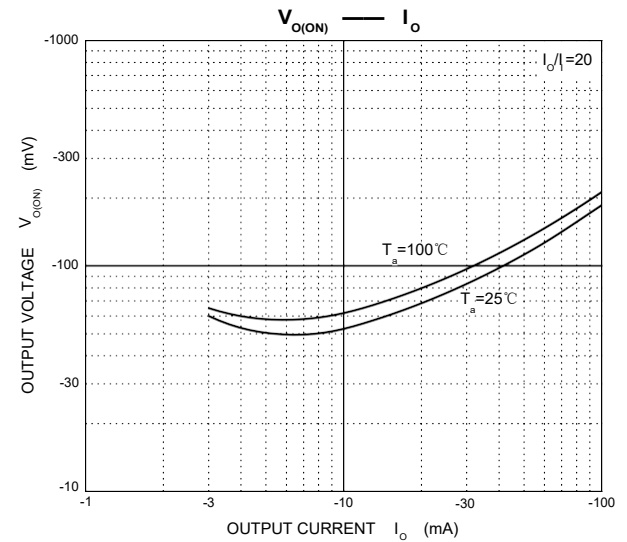
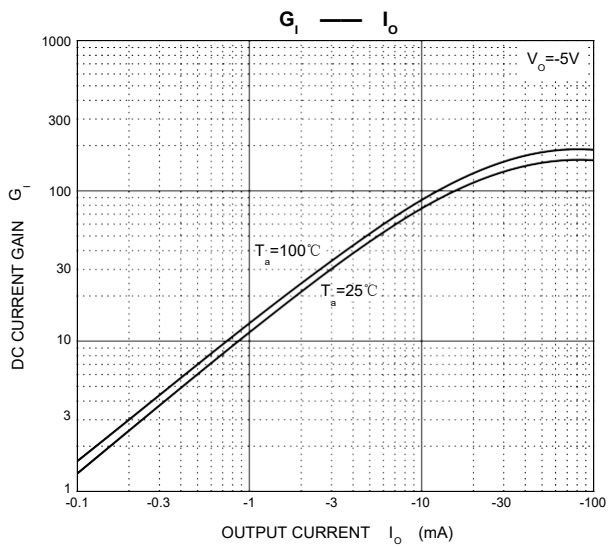
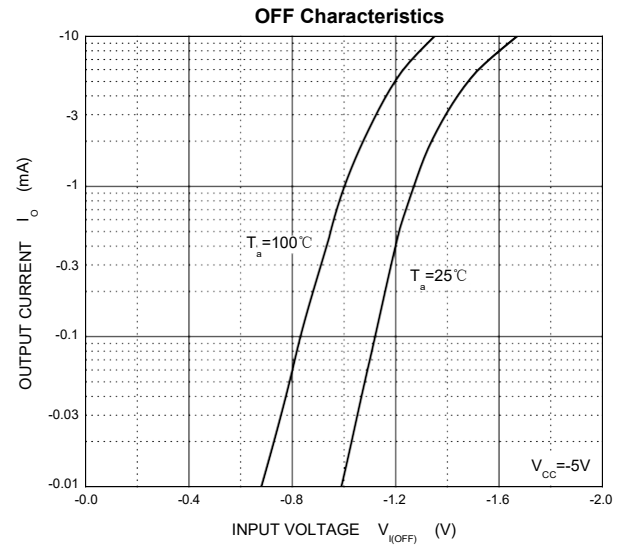
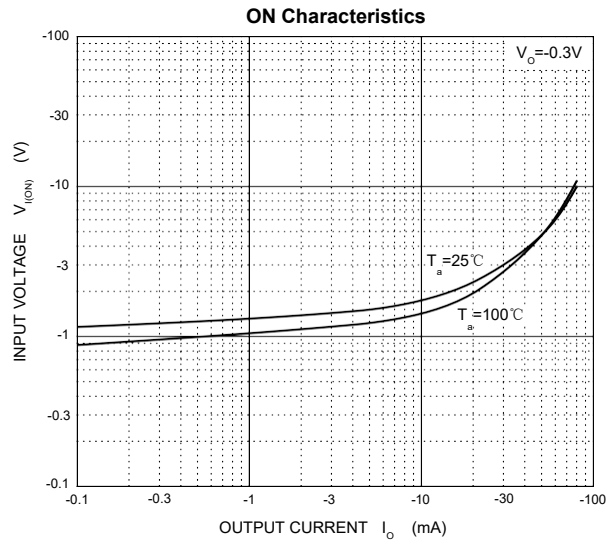
MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	-50	V
V_{IN}	Input Voltage	-40~+10	V
I_O	Output Current	-50	mA
I_{CM}	Peak Collector Current	-100	mA
P_D	Power Dissipation	200	mW
T_j	Junction Temperature	150	°C
T_{stg}	Storage Temperature	-55~+150	°C

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	$V_{I(off)}$	$V_{CC}=-5V, I_O=-100\mu A$	-0.5			V
	$V_{I(on)}$	$V_O=-0.3V, I_O=-10mA$			-3	V
Output voltage	$V_{O(on)}$	$I_O/I_I=-10mA/-0.5mA$			-0.3	V
Input current	I_I	$V_I=-5V$			-0.88	mA
Output current	$I_{O(off)}$	$V_{CC}=-50V, V_I=0$			-0.5	μA
DC current gain	G_I	$V_O=-5V, I_O=-5mA$	30			
Input resistance	R_1		7	10	13	k Ω
Resistance ratio	R_2/R_1		0.8	1	1.2	
Transition frequency	f_T	$V_O=-10V, I_O=-5mA, f=100MHz$		250		MHz

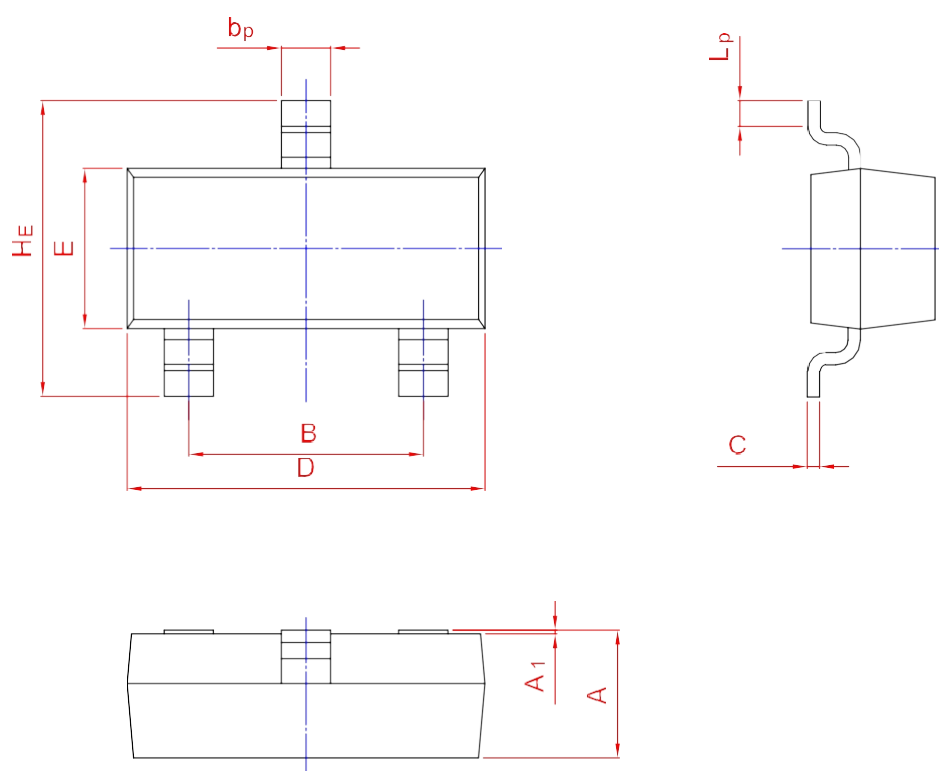
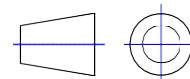
Typical Characteristics



PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



UNIT	A	B	b _p	C	D	E	H _E	A ₁	L _p
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20