

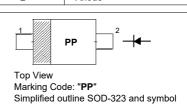
1N728WS

SILICON EPITAXIAL PLANAR SCHOTTKY BARRIER DIODE

For super-high speed switching and wave detection circuit applications

PINNING

| PIN | DESCRIPTION |
|-----|-------------|
| 1 | Cathode |
| 2 | Anode |



Absolute Maximum Ratings ($T_a = 25 \circ C$)

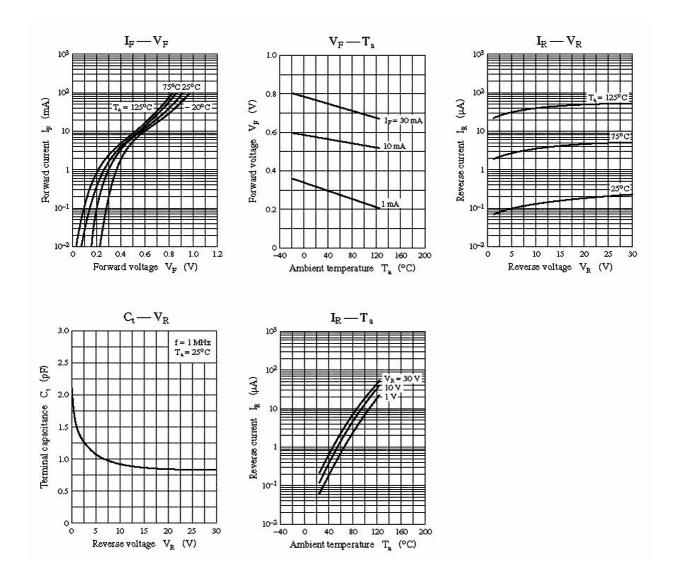
| Parameter | Symbol | Value | Unit |
|---------------------------|------------------|---------------|------|
| Peak Reverse Voltage | V _{RM} | 30 | V |
| Reverse Voltage | V_{R} | 30 | V |
| Forward Current | lf | 30 | mA |
| Peak Forward Current | Іғм | 150 | mA |
| Junction Temperature | Tj | 125 | oC |
| Storage Temperature Range | T _{stg} | - 55 to + 125 | °C |

Characteristics at T_a = 25 ∘C

| Parameter | Symbol | Тур. | Max. | Unit |
|--|-----------------|------|------|------|
| Forward Voltage at I _F = 1 mA | V _F | - | 0.4 | V |
| at I _F = 30 mA | | - | 1 | |
| Reverse Current at V _R = 30 V | I _R | 1 | 0.3 | μΑ |
| Terminal Capacitance at V _R = 1 V, f = 1 MHz | Ст | 1.5 | 1 | pF |
| Reverse Recovery Time at $I_F = I_R = 10$ mA, $I_{rr} = 1$ mA, $R_L = 100$ Ω | t _{rr} | 1 | 1 | ns |
| Detection Efficiency at V_{in} = 3 $V_{(peak)}$, f = 30 MHz, R_L = 3.9 K Ω , C_L = 10 pF | η | 65 | - | % |



Typical Characteristics





PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD-323

